

IN THE CLAIMS

Please amend claims 1-9 as indicated below.

Please add new claims 25-45 as indicated below.

1. (Currently Amended) A method comprising:
performing a refinement coding pass to bit-planes of a code block to create refinement bits, wherein the refinement bits are generated based on bits from coefficients that became significant in a significance propagation pass of a previous bit-plane; and
setting refinement bits to the more probable symbol (MPS), wherein the refinement bits set to the MPS are quantized as a result of setting refinement bits to the MPS.
2. (Currently Amended) The method defined in Claim 1 wherein only a portion of the refinement bits in a ~~code block~~ code block are set to the MPS, and wherein the portion of the refinement bits set to the MPS is transmitted having a number of bits different than a number of bits of a remaining portion of the refinement bits.
3. (Currently Amended) The method defined in Claim 1 wherein setting refinement bits to the MPS is performed to implement non-uniform quantization step sizes, wherein the refinement bits set to the MPS are quantized to a value different from values of the refinement bits without being set to the MPS within the code block.
4. (Currently Amended) An apparatus comprising:

means for performing a refinement coding pass to bit-planes of a code block to create refinement bits, wherein the refinement bits are generated based on bits from coefficients that became significant in a significance propagation pass of a previous bit-plane; and

means for setting refinement bits to the more probable symbol (MPS), wherein the refinement bits set to the MPS are quantized as a result of setting refinement bits to the MPS.

5. (Currently Amended) The apparatus defined in Claim 4 wherein only a portion of the refinement bits in a ~~codeblock~~ code block are set to the MPS, and wherein the portion of the refinement bits set to the MPS is transmitted having a number of bits different than a number of bits of a remaining portion of the refinement bits.

6. (Currently Amended) The apparatus defined in Claim 4 wherein setting refinement bits to the MPS is performed to implement non-uniform quantization step sizes, wherein the refinement bits set to the MPS are quantized to a value different from values of the refinement bits without being set to the MPS within the code block.

7. (Currently Amended) An article of manufacture comprising at least one recordable media storing executable instructions thereon which, when executed by a processing device, cause the processing device to:

perform a refinement coding pass to bit-planes of a code block to create refinement bits, wherein the refinement bits are generated based on bits from coefficients that became significant in a significance propagation pass of a previous bit-plane; and

set refinement bits to the more probable symbol (MPS), wherein the refinement bits set to the MPS are quantized as a result of setting refinement bits to the MPS.

8. (Currently Amended) The article of manufacture defined in Claim 7 wherein only a portion of the refinement bits in a ~~codeblock~~ code block are set to the MPS, and wherein the portion of the refinement bits set to the MPS is transmitted having a number of bits different than a number of bits of a remaining portion of the refinement bits.

9. (Currently Amended) The article of manufacture defined in Claim 7 wherein setting refinement bits to the MPS is performed to implement non-uniform quantization step sizes, wherein the refinement bits set to the MPS are quantized to a value different from values of the refinement bits without being set to the MPS within the code block.

10. – 24. (Canceled)

25. (New) The method defined in Claim 1, further comprising identifying a target area of the refinement bits of the code block that is designated to maintain a predetermined quality of the target area, wherein setting the refinement bits to the MPS comprises

setting the refinement bits of the code block that do not effect the predetermined quality of the target area to the MPS, while using actual values for the refinement bits that effect the predetermined quality of the target area.

26. (New) The method defined in Claim 25, wherein the target area comprises text in a background image, wherein the refinement bits that are set to MPS are those that do not effect the text of the background image for the last bitplane, while using the actual value for the refinement bits that effect the text of the background image.

27. (New) The method defined in Claim 1, wherein the refinement bits are set to the MPS to reduce distortion effects on a current frame due to a change of a rate distortion of the corresponding refinement bits of a previous frame.

28. (New) The method defined in Claim 1, wherein the refinement bits are set to the MPS to minimize distortion effects on a current frame due to a change of a rate distortion of the corresponding refinement bits of a previous frame.

29. (New) The apparatus defined in Claim 4, further comprising means for identifying a target area of the refinement bits of the code block that is designated to maintain a predetermined quality of the target area, wherein means for setting the refinement bits to the MPS comprises

means for setting the refinement bits of the code block that do not effect the predetermined quality of the target area to the MPS, while using actual values for the refinement bits that effect the predetermined quality of the target area.

30. (New) The apparatus defined in Claim 29, wherein the target area comprises text in a background image, wherein the refinement bits that are set to the MPS are those that do not effect the text of the background image for the last bitplane, while using the actual value for the refinement bits that effect the text of the background image.

31. (New) The apparatus defined in Claim 4, wherein the refinement bits are set to the MPS to reduce distortion effects on a current frame due to a change of a rate distortion of the corresponding refinement bits of a previous frame.

32. (New) The apparatus defined in Claim 4, wherein the refinement bits are set to the MPS to minimize distortion effects on a current frame due to a change of a rate distortion of the corresponding refinement bits of a previous frame.

33. (New) The article of manufacture defined in Claim 7, further comprising identifying a target area of the refinement bits of the code block that is designated to maintain a predetermined quality of the target area, wherein setting the refinement bits to the MPS comprises

setting the refinement bits of the code block that do not effect the predetermined quality of the target area to the MPS, while using actual values for the refinement bits that effect the predetermined quality of the target area.

34. (New) The article of manufacture defined in Claim 33, wherein the target area comprises text in a background image, wherein the refinement bits that are set to MPS are those that do not effect the text of the background image for the last bitplane, while using the actual value for the refinement bits that effect the text of the background image.

35. (New) The article of manufacture defined in Claim 7, wherein the refinement bits are set to the MPS to reduce distortion effects on a current frame due to a change of a rate distortion of the corresponding refinement bits of a previous frame.

36. (New) The article of manufacture defined in Claim 7, wherein the refinement bits are set to the MPS to minimize distortion effects on a current frame due to a change of a rate distortion of the corresponding refinement bits of a previous frame.

37. (New) A method, comprising:

identifying a target region of refinement bits of a code block generated from a magnitude refinement pass based on bits from significant coefficients of a significance propagation pass of a previous bit-plane; and

setting one or more refinement bits of the code block that do not significantly effect resolution of the target region to a more probable symbol (MPS), such that the one or more refinement bits are quantized with respect to other refinement bits that effect the resolution of the target region.

38. (New) The method of claim 37, wherein the refinement bits that effect the resolution of the target region are set using actual values of the respective refinement bits.

39. (New) The method of claim 37, wherein the refinement bits set to the MPS are quantized to a value different from values of the refinement bits without being set to the MPS within the code block.

40. (New) The method of claim 37, wherein the portion of the refinement bits set to the MPS is transmitted having a number of bits different than a number of bits of a remaining portion of the refinement bits.

41. (New) The method of claim 37, wherein the refinement bits are set to the MPS to reduce distortion effects on a current frame due to a change of a rate distortion of the corresponding refinement bits of a previous frame.

42. (New) The method of claim 37, wherein the refinement bits are set to the MPS to minimize distortion effects on a current frame due to a change of a rate distortion of the corresponding refinement bits of a previous frame.

43. (New) The method of claim 37, wherein the target region comprises text in a background image, wherein the refinement bits that are set to MPS are those that do not effect the text of the background image for the last bitplane, while using the actual value for the refinement bits that effect the text of the background image.

44. (New) An article of manufacture comprising at least one recordable media storing executable instructions thereon which, when executed by a processing device, cause the processing device to:

identify a target region of refinement bits of a code block generated from a magnitude refinement pass based on bits from significant coefficients of a significance propagation pass of a previous bit-plane, and

set one or more refinement bits of the code block that do not significantly effect resolution of the target region to a more probable symbol (MPS), such that the one or more refinement bits are quantized with respect to other refinement bits that effect the resolution of the target region.

45. (New) An apparatus, comprising:

means for identifying a target region of refinement bits of a code block generated from a magnitude refinement pass based on bits from significant coefficients of a significance propagation pass of a previous bit-plane; and

means for setting one or more refinement bits of the code block that do not significantly effect resolution of the target region to a more probable symbol (MPS), such that the one or more refinement bits are quantized with respect to other refinement bits that effect the resolution of the target region.